



Product Overview

This document provides information regarding the AVT-DNIX-FMC-INTR (Interfaces & Peripherals) module. The module is a VITA 57.1 compliant single width FMC HPC, designed for use with DNIX modules and more 57.1 compliant carrier cards.

Block Diagram

The AVT-DNIX-FMC-INTR board block diagram shown in Figure 1.

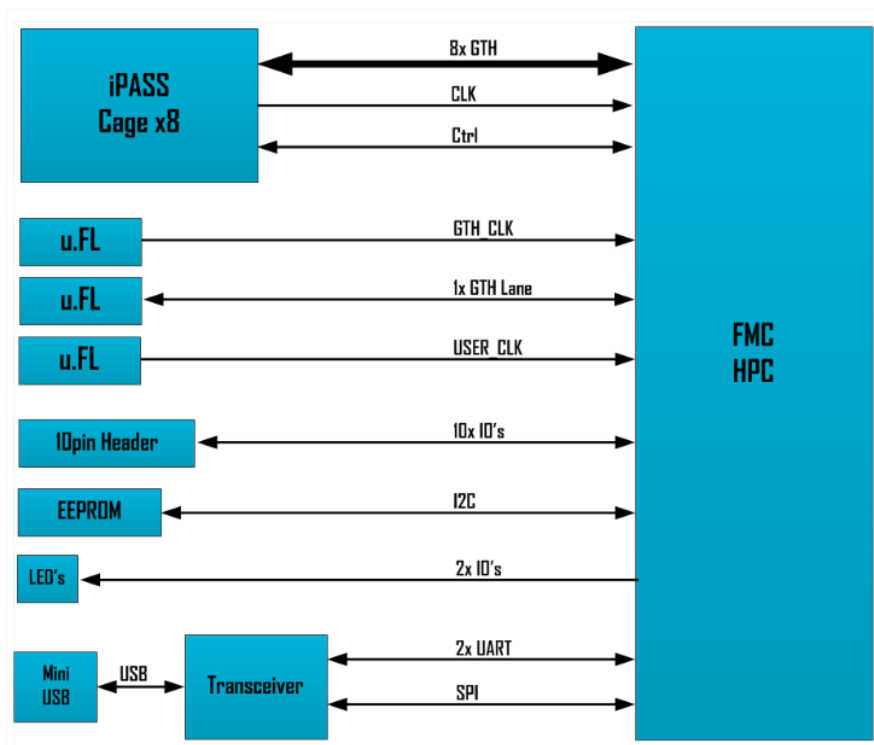


FIGURE 1 - AVT-DNIX-FMC-INTR BLOCK DIAGRAM

Main Features

- VITA 57.1 FMC HPC connector
- GbE Interface
- SATA Interface
- USB Interface
- 4x u.FL connector for GTH lane (TX and RX)
- 2x u.FL connector for User clock
- Lauterbach JTAG Header
- PMOD Interface
- 10pin Header for User
- Front Panel LED's
- 2x UART interface (via Transceiver)
- 1x SPI interface (via Transceiver)
- On board clock oscillator for GTH ref clock

Ordering Information

Parts

Part Number	Description	Price
AVT-ONIX-FMC-INTR-G	FMC to Interfaces & Peripherals	Contact your local distributor

Related Products

Part Number	Description	Price
AVT-ONIX-VU440-1	ONIX Module XCVU440 -1 Device	Contact ONIX
AVT-ONIX-VU440-2	ONIX Module XCVU440 -2 Device	Contact ONIX